

BIOS Revision Notes

Target Board-Name: PPM-C407
Target Board-Number: 407
Initial Release Date: 6-29-15

Change History Log:

Note: The Min. PCB Rev Rqd field specifies the minimum PCB Revision and assumes all current ECO's have been applied.

Date	Time	Version	CKSM	(Min. PCB Rev Rqd)	Description
06/29/2015	11:33:27	40715177	0x33A5	E	Original Release
08/14/2015	15:09:14	40715225	0xACB7	E	ECO #15-27 Restored COM port configuration in ACPI code Fixed programming of config bits for COM3/4 Removed Optimal Default selection on Exit menu Added FpgaProg shell utility
11/16/2015	16:18:36	40715320	0xDDDE	E	ECO #15-41 Fixed memory size display for 4 GB boards C-States disabled by default Remapped GPIO from 0x500 to base address 0x800 Added LvdsProg shell utility for CC405 + C407
03/11/2016	13:25:29	40716071	0x533B	F	ECO #16-19 Updated to Phoenix SCT version 3.1.0.647 Updated to Phoenix Silicon version 1.0.0.279 Changed default IRQ for COM+DIO ports to 0 (Disabled) Fixed Watchdog Timer Enabled PCIe wake-up Added Windows 10 support to ACPI tables

07/05/16	10:58:56	40716187	0x6F8F		F	ECO #16-42 Updated TXE firmware to version 1.1.1.1120 Added CPUID test to determine memory speed Added PXE Boot support Reduced BIOS menu selections to match CC405 Extended Thermal selections for passive + critical thresholds Changed some CPU thermal register settings Updated ACPI code to match device Updated microcode to M0130679907 Changed default settings of Super I/O Fixed DOS keyboard repeat bug
07/20/16	11:21:18	40716202	0xE705		F	ECO #16-61 Fixed ACPI error with Super I/O
05/25/2017	14:23:17	40717145	0x754B	PBD	F	ECO #17-39 CTRL+C function under legacy O/S fixed XHCI boot issue fixed COM port default values added when disabled Changed Boot Display device values Removed AUX Polarity menu item Replaced IRQ11 with IRQ14 as option for DIO New VBIOS and VBE GOP ROM images
02/08/2019	09:11:27	40719039	0x9D76	PBD	F	ECO #19-xx Added support for new flash device - Winbond W25Q64FW Updated D0 stepping microcode Added D1 stepping microcode Added ACPI support for UIO and WDT WinBase Route 19r204 Removed ACPI code additions from 40719039 version - caused problems with existing drivers
03/15/2019	11:27:54	40719074	0x9260	PBD	F	WinBase Route 19r363 Forced SPI Flash speed to 20 MHz - needed due to MUX in path
04/25/2019	10:07:10	40719115	0x1802	PBD	F	WinBase Route 20r09E Source code upgrade to 3.1.0.850 TXE upgrade to 1.1.4.1145 Fixed PXE boot for i210 Fixed console issue in CSM mode Advanced menu items - some commented out, now are suppressed Disabled build option to ensure LPC clock on continuously
02/04/2020	17:10:35	40720035	0x67A99E49	PBD	F	WinBase Route 20r175 Enabled HDA and LPE in FIT tool
05/22/2020	09:31:07	40720142	0xED400B94	PBD	F	